

MPW Shuttle Service 2017

Schedule

Process	Data and Order to	Tape In ¹⁾	Samples Out ²⁾
0.18 µm HV-CMOS 20V / 25V / 50V ³⁾			
aH18	ams	27-Feb-17	22-May-17
aH18	ams	29-May-17	21-Aug-17
aH18	ams	28-Aug-17	12-Feb-18
aH18	ams	4-Dec-17	18-May-18
0.18 µm CMOS 1.8V / 5V ³⁾			
aC18	ams	27-Feb-17	22-May-17
aC18	ams	29-May-17	21-Aug-17
aC18	ams	28-Aug-17	12-Feb-18
aC18	ams	4-Dec-17	18-May-18
0.35 µm HV-CMOS 20V / 50V / 120V (embedded Flash ⁴⁾)			
H35B4D3	ams	20-Feb-17	21-Apr-17
H35B4D3	ams	8-May-17	07-Jul-17
H35B4D3	ams	21-Aug-17	18-Dec-17
H35B4D3	ams	6-Nov-17	26-Feb-18
0.35 µm CMOS 3.3V / 5V (embedded Flash ⁴⁾) and 0.35 µm Opto-CMOS ⁵⁾			
C35B4xx ⁶⁾	ams	6-Feb-17	24-Mar-17
C35B4xx ^{6) 5) 7)}	Fraunhofer IIS	24-Apr-17	30-Jun-17
C35B4xx ⁶⁾	ams	19-Jun-17	4-Aug-17
C35B4xx ^{6) 5) 7)}	Fraunhofer IIS	07-Aug-17	06-Nov-17
C35B4xx ⁶⁾	ams	2-Oct-17	5-Jan-18
C35B4xx ^{6) 5) 7)}	Fraunhofer IIS	20-Nov-17	19-Feb-18
0.35 µm SiGe-BiCMOS 3.3V / 5V 2P/4M			
S35D4M5	ams	6-Mar-17	12-May-17
S35D4M5	ams	12-Jun-17	18-Aug-17
S35D4M5	ams	18-Sep-17	31-Jan-18
S35D4M5	ams	18-Dec-17	30-Apr-18

Additional MPW Shuttles are scheduled by our partners

Fraunhofer IIS
www.iis.fraunhofer.de



Europractice
www.europractice-ic.com



CMP
cmp.imag.fr



MOSIS
www.mosis.com



MEDS Technologies
www-meds-tech.com



TOPPAN
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Services

Technologies available for MPW service

Process	Technology	Voltage Levels		Issue August 2017	
				Available Options	
aH18	0.18 µm HV-CMOS	1.8/5V Gates	1.8/5/20/25/50V FETs	RR, RP, NE, PE, QT	6 Metal AM, 1 Poly
aC18	0.18 µm CMOS	1.8/5V Gates	1.8/5V FETs	RR, RP, NE, PE, QT	6 Metal AM, 1 Poly
H35	0.35 µm HV-CMOS	3.3/5/20V Gates	3.3/5/20/50/120V FETs	HR, PIP	4 Metal, 2 Poly
C35	0.35 µm CMOS	3.3/5V Gates	3.3/5 FETs	HR, PIP	4 Metal, 2 Poly
S35	0.35 µm SiGe-BiCMOS	3.3/5V Gates	3.3/5/12V HBTs	HR, PIP, MIM	4 Metal, 2 Poly

The participation in the ams MPW service includes the delivery of 40 dice.

Process stops, wafer probe tests or wafer shipments are not possible for MPWs.

Additional Services

Comment

Additional Dice	Up to 100 possible from single placement on MPW
Ceramic Packages	10 parts with taped lid
Scoop & Goop Packages	10 parts / availability check necessary at tape in
Plastic Packages	50 parts with up to 84 pins / requires an additional cycle time of 2 to 3 weeks
WLCSP with RDL	WLCSP (Balls 250µm, 400µm min. pitch) with redistribution layer available on request (0.35µm only)
0.35 µm CMOS with Embedded Flash	High reliability EEPROM and Flash with block sizes from 1kx8 to 32kx16 bit
0.35 µm HV-CMOS with Embedded Flash	High reliability EEPROM and Flash with block sizes from 1kx8 - 4kx16 bit
0.35 µm Opto CMOS	Opto process with optional EPI wafer startmaterial and antireflective coating layer

Notes

The total cycle time includes MPW cluster preparation, wafer manufacturing & back-lapping, dicing, assembling in ceramic or in scoop&goop package.

¹⁾ Please provide a completed Foundry Service Request Form (ENG-21; s.link below "Technical Information"), when providing a database.

²⁾ The Sample Out dates are without obligation.

³⁾ Please note that other process options than listed in "Available Options" are only available for single die toolings.

⁴⁾ EEPROM resp. Flash option available on request for every MPW-start in 0.35µm CMOS and 0.35µm HV-CMOS technology.

⁵⁾ Opto option available on request. Opto CMOS process only available for 2 poly / 4 metal.

⁶⁾ Represents different process options in ams Fab B (exception: RF-CMOS, e.g. C35B4M3).

⁷⁾ Opto CMOS process with ARC/BARC

Disclaimer

No implied offer: The usage of ams design kits and/or libraries or the participation on Multi Project Wafer runs shall not be regarded as an implied offer by ams to subsequently manufacture such integrated circuit. In any case each integrated circuit, which is planned for production, requires an official quotation by ams.

Delivery dates are subject to change without notice.

Information

Foundry Support Server: asic.ams.com
Tape-out form: [Foundry Request Form](#)
Quote: [Online Tool for registered Users](#)

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